## BG-simulation and Renaming

Julien Stainer

Concurrent Algorithms Distributed Programming Laboratory julien.stainer@epfl.ch





## Outline

#### **1** BG-Simulation

Problem Statement Simulation Algorithm Safe-Agreement Putting Pieces Together Computability Consequences

#### 2 Renaming

Problem Statement Renaming and Crashes A Splitter-based Algorithm A Snapshot-based Algorithm

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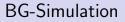
#### **1** BG-Simulation

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# BG-Simulation The BG-Simulation algorithm allows to wait-free simulate a *t*-resilient system of *n* asynchronous processes sharing memory with t + 1 asynchronous simulators sharing memory.



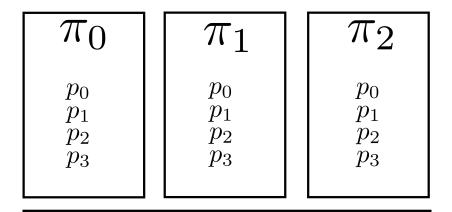
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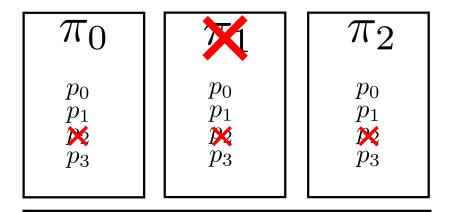
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We must ensure that, in any simulated execution, at most *t* simulated processes crash.



#### Shared memory



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- each simulator π<sub>s</sub> simulates all the processes
   p<sub>0</sub>,..., p<sub>n-1</sub>;
- it computes an output *output<sub>i</sub>* for each process that do not crash during the simulation.

We consider deterministic programs *prog*<sub>i</sub> supposed to be of the following form:

```
state_i \leftarrow init_i

while not_decided(state_i) do

val \leftarrow next_write(state_i)

write(val, MEM[i])

snap_i \leftarrow snapshot(MEM)

state_i \leftarrow update_state(snap_i, state_i)

end while
```

**return** compute\_output(*state*<sub>i</sub>)

## Simulation Algorithm

For each process  $p_i$ , simulator  $\pi_s$  maintains:

• *p<sub>i</sub>*'s state: *state*[*i*]

(values of variables, instruction pointer, etc.);

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- Threads do not crash individually.

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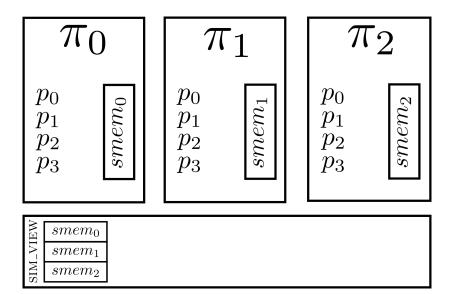
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- *smem<sub>s</sub>* is an array of *n* elements, one for each simulated process;
- smem<sub>s</sub>[i] is the last value written by p<sub>i</sub> in its simulation by π<sub>s</sub>;

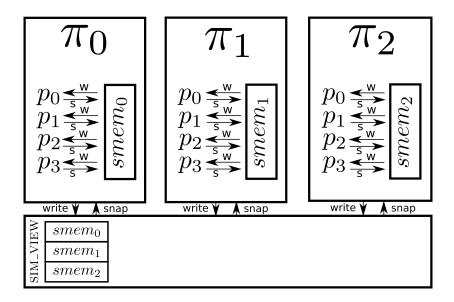
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- SIM\_VIEW[s'][i] is a pair containing the last value written by  $p_i$  according to simulator  $\pi_{s'}$ , and the corresponding sequence number.





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We need to ensure the atomicity of the simulated shared memory.

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In a crash-free system, safe-agreement objects implement consensus.

### Safe-Agreement

- 1: init  $REG[0, \ldots, n-1] \leftarrow [\langle \bot, 0 \rangle]$
- 2: operation PROPOSE(v)

3: 
$$REG[s] \leftarrow \langle v, 1 \rangle$$

4: 
$$snap_s \leftarrow REG.snapshot()$$

5: **if**  $\exists x : snap_s[x]$ .level = 2 **then** 

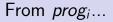
6: 
$$REG[s] \leftarrow \langle v, 0 \rangle$$

7: **else** 

8: 
$$REG[s] \leftarrow \langle v, 2 \rangle$$

- 9: end if
- 10: end operation
- 11: operation DECIDE()
- 12: repeat
- 13:  $snap_s \leftarrow REG.snapshot()$
- 14: **until**  $\forall x$  :  $snap_s[x]$ . level  $\neq 1$
- 15:  $x \leftarrow \min\{y \mid snap_s[y] = 2\}$
- 16: return  $snap_s[x]$ . value
- 17: end operation

$\pi_0$	$\pi_1$	$\pi_2$
$\begin{array}{c} p_{0} \underbrace{\overset{w}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{w}{\overset{s}{\overset{w}{\overset{w}{\overset{s}{\overset{w}{\overset{w}{\overset{s}{\overset{w}}{\overset{w}{\overset{w}{\overset{w}{\overset{w}{\overset{w}}{\overset{w}{\overset{w}{\overset{w}}}}}}}}}$	$\begin{array}{c} p_0 \underbrace{\overset{w}{\overset{s}{\overset{w}}}}_{p_1} \mathbf{p}_1 \underbrace{\overset{w}{\overset{s}{\overset{w}}}}_{p_2} \mathbf{p}_3 \underbrace{\overset{w}{\overset{s}{\overset{w}}}}_{s} \mathbf{p}_3 \underbrace{\overset{w}{\overset{s}{\overset{w}}}}_{s} \mathbf{p}_3 \underbrace{\overset{w}{\overset{s}{\overset{w}}}}_{s} \mathbf{p}_3 \underbrace{\overset{w}{\overset{w}{\overset{s}{\overset{w}}}}}_{s} \mathbf{p}_3 \underbrace{\overset{w}}}}}}}}}$	$\begin{array}{c} p_0 \underbrace{\overset{w}{\underset{s}{\overset{w}{\overset{s}{\overset{w}{w$
write 🖌 🗼 snap	write 🔰 🗼 snap	write 🖌 🗼 snap
$\mathbb{A}$ $smem_0$ $\mathbb{A}$ $\mathbb{B}$	$SAFE\_AGR[0][0]$ $SAFE\_AGR[0][1]$	$SAFE\_AGR[0][2]$
$> emom_{\star}$	SAFE_AGR[1][0] SAFE_AGR[1][1]	SAFE_AGR[1][2]
$\begin{array}{c} \text{Smem}_1\\ \text{Smem}_2 \end{array} \qquad $	SAFE_AGR[2][0] SAFE_AGR[2][1]	SAFE_AGR[2][2]
IS Smem <sup>5</sup> SA	$SAFE\_AGR[3][0]$ $SAFE\_AGR[3][1]$	$SAFE\_AGR[3][2]$



 $state_i \leftarrow init_i$ while not\_decided(*state<sub>i</sub>*) **do**  $val \leftarrow next_write(state_i)$ write(val, MEM[i])  $snap_i \leftarrow snapshot(MEM)$  $state_i \leftarrow update_state(snap_i, state_i)$ end while **return** compute\_output(*state*;)

### ... to the Simulation Thread

 $state_i \leftarrow init_i$ ;  $write\_sn[i] \leftarrow 0$ ;  $snap\_sn[i] \leftarrow 0$ while not\_decided(*state<sub>i</sub>*) do  $val \leftarrow next_write(state_i)$ simulate\_write(i, val, MEM[i])  $snap_i \leftarrow simulate\_snapshot(i, MEM)$  $state_i \leftarrow update_state(snap_i, state_i)$ end while **return** compute\_output(*state*<sub>i</sub>)

## **operation** SIMULATE\_WRITE(*i*,*val*,MEM[*i*]) *write\_sn*[*i*] $\leftarrow$ *write\_sn*[*i*] + 1 *smem\_s*[*i*] $\leftarrow$ (*val*, *write\_sn*[*i*]) SIM\_VIEW[*s*] $\leftarrow$ *smem\_s* **end operation**

### Simulating Snapshots

```
operation SIMULATE_SNAPSHOT(i, MEM)
   snap \leftarrow SIM_VIEW.snapshot()
   for x \in \{1, ..., n\} do
       let z be s.t. \forall y, snap[z][x].sn > snap[y][x].sn
       sim\_snap[x] \leftarrow snap[z][x]
   end for
   snap_sn[i] \leftarrow snap_sn[i] + 1
   SAFE_AGR[i][snap_sn[i]].PROPOSE(sim_snap)
   return SAFE_AGR[i][snap_sn[i]].DECIDE()
end operation
```

### Coherence and Atomicity

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- A simulated write is linearized at the first moment a simulator π<sub>s</sub> writes a smem<sub>s</sub> containing this write into SIM\_VIEW[s].

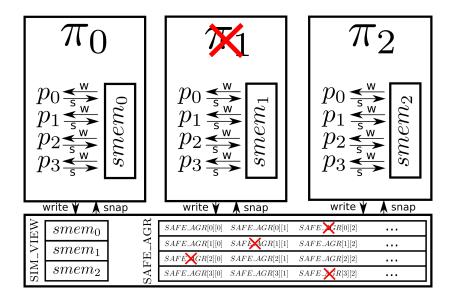
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- A simulated write is linearized at the first moment a simulator π<sub>s</sub> writes a smem<sub>s</sub> containing this write into SIM\_VIEW[s].
- A simulated snapshot is linearized at the moment the simulator that proposes it to the safe-agreement took its corresponding snapshot of SIM\_VIEW.

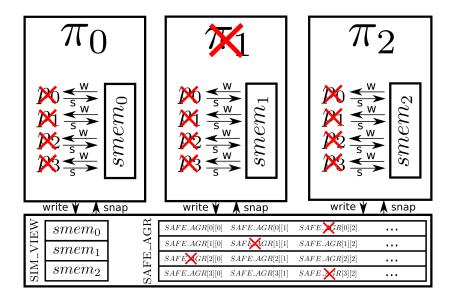
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- If it crashes at that point, DECIDE operations of these objects may block forever for all simulators;
- the corresponding simulated processes then stop making progress.

$\pi_0$	<b>X</b>	$\pi_2$
$\begin{array}{c} p_{0} \underbrace{\overset{w}{\overset{s}{\overset{w}}}}_{p_{1}} \underbrace{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{w}{\overset{s}{\overset{w}{\overset{w}{\overset{s}{\overset{w}}{\overset{w}{\overset{w}{\overset{w}{\overset{w}{\overset{w}}{\overset{w}{\overset{w}{\overset{w}{\overset{w}}}}}}}}}$	$\begin{array}{c} p_0 \underbrace{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{s}{\overset{w}{\overset{w}{\overset{s}{\overset{w}{\overset{w}{\overset{w}{\overset{s}{\overset{w}}{\overset{w}{\overset{w}{\overset{w}{\overset{w}{\overset{w}}{\overset{w}{\overset{w}{\overset{w}{\overset{w}{\overset{w}{\overset{w}{\overset{w}{\overset{w}{\overset{w}}{\overset{w}{\overset{w}}}}}}}}}$	$\begin{array}{c} p_0 \underbrace{\overset{w}{\underset{s}{\overset{w}{\overset{s}{\overset{w}{w$
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# All we need is local synchronization!

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We can easily force a thread never to be in more than one PROPOSE operation.

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operation SIMULATE_SNAPSHOT(i, MEM)
   snap \leftarrow SIM_VIEW.snapshot()
   for x \in \{1, ..., n\} do
       let z be s.t. \forall y, snap[z][x].sn \geq snap[y][x].sn
       sim_snap[x] \leftarrow snap[z][x]
   end for
   snap_sn[i] \leftarrow snap_sn[i] + 1
   enter mutex
   SAFE_AGR[i][snap_sn[i]].PROPOSE(sim_snap)
   leave mutex
   return SAFE_AGR[i][snap_sn[i]].DECIDE()
end operation
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The crash of a simulator can prevent the progress of at most one simulated process.

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- The correct simulators can then compute an output for each simulated process that do not block in the simulation.

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- δ : I → 2<sup>O</sup> is a function that, to any input configuration *I* ∈ I, associates the set δ(*I*) ⊆ O of the output configurations that are allowed when starting from *I*.

#### **Decision Tasks**

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• Consensus, k-set agreement are decision tasks, renaming isn't.

Any decision task that we can solve with *n* processes and *t* crashes, we can solve it with t + 1 processes and *t* crashes. The study of decision tasks computability can be reduced to the n - 1-resilient case.



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- This solves *t*-set agreement between our t + 1 simulators.
- Contradiction, so there is no such algorithm.

# What matters is the number of crashes, not the number of processes.

• Borowsky, E., Gafni, E., Lynch, N., Rajsbaum, S.: *The BG distributed simulation algorithm*. Distributed Computing 14(3), 127146 (2001).

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- Gafni, E.: *The Extended BG Simulation and the Characterization of t-Resiliency*. STOC 2009.

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- Damien Imbs, Michel Raynal: Visiting Gafni's Reduction Land: From the BG Simulation to the Extended BG Simulation. SSS 2009.

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#### The Renaming Problem

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- up to n-1 of them may crash;

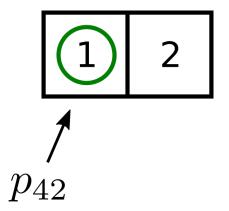
- *n* asynchronous processes sharing atomic registers;
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- they are given names in a large namespace  $\{-N, \ldots, N\}, N >> n;$

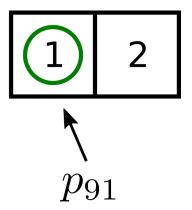
- *n* asynchronous processes sharing atomic registers;
- up to n-1 of them may crash;
- they are given names in a large namespace  $\{-N, \ldots, N\}, N >> n;$
- k-renaming provides them with a GET\_NAME operation that returns a new unique name in a smaller namespace {1,...,k}.

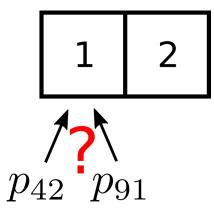
• Using shorter names spares bandwidth, memory, storage, etc.

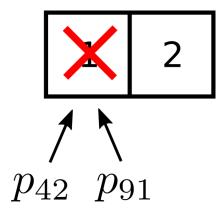
- Using shorter names spares bandwidth, memory, storage, etc.
- The "big" names are just a way to break symmetry, renaming protocols allow to dynamically compute unique identifiers.

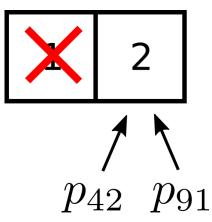
- Using shorter names spares bandwidth, memory, storage, etc.
- The "big" names are just a way to break symmetry, renaming protocols allow to dynamically compute unique identifiers.
- Several problems can be reduced to renaming (e.g. picking a unique transmitting frequency).

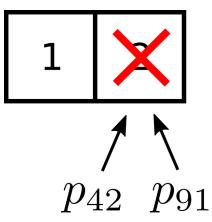


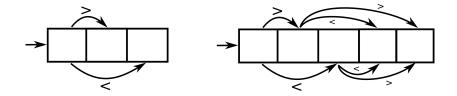


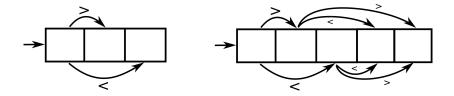


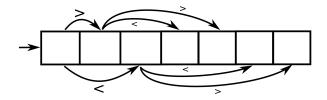


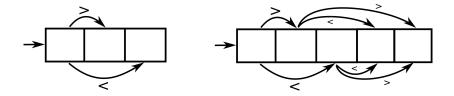


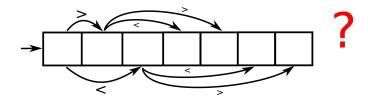












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#### Adaptive Renaming

Ideally the protocol should be adaptive: the largest name obtained should depend on the actual number of participating processes, not on the total number of processes.

### The Splitter Object

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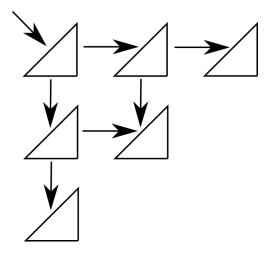
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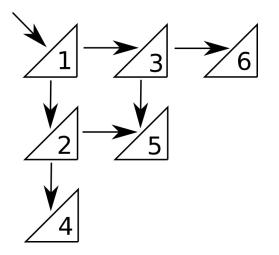
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- If *x* processes invoke DIRECTION:
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  - at most one obtains *stop*.

• Any invocation to **DIRECTION** by a correct process terminates.



#### Splitter-based Renaming

```
operation NEW_NAME(id)
    d \leftarrow 1; r \leftarrow 1; move \leftarrow down
    while move \neq stop do move \leftarrow S[d, r].DIRECTION(id);
        if move = right then
            r \leftarrow r + 1
        else if move = down then
            d \leftarrow d + 1
        end if
    end while
    return (d + r - 1)(d + r - 2)/2 + r
end operation
```



# Great! But we can do even better.

#### Snapshot-based Renaming

```
operation NEW_NAME(id)
    name \leftarrow 1
    while true do
        MEM[i] \leftarrow \langle id, name \rangle
        snap \leftarrow MEM.snapshot()
        if \forall j \neq i : snap[j].name \neq name then
             return name
        else
             free \leftarrow names of \{1, \ldots, \infty\} that do not appear in snap
             rank \leftarrow rank of id in the set of identifiers appearing in snap
             name \leftarrow name at position rank in free
        end if
    end while
end operation
```

#### **Proof Elements**

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Termination If a set of processes never decides, their *rank* variables will stabilize on distinct values.

The one with the smallest *rank* is then eventually alone to propose its new name. Contradiction.

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• Renaming algorithms distribute new unique names from a smaller namespace.