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Concurrent programming: From theory to practice Concurrent Algorithms 2021

Vasileios Trigonakis Principal Researcher Oracle Labs Zurich

13.Dec.2021

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Vasileios Trigonakis

- Principal Researcher @ Oracle Labs
- PhD in Computer Science from EPFL
- Started at Oracle in 2016
- Leading the PGX Distributed (PGX.D) project



From theory to practice

Theoretical (design)

Practical (design)

Practical (implementation)

- Impossibilities
- Upper/Lower bounds
- Techniques
- System models
- Correctness proofs

Design

(pseudo-code)

- System models
 - shared memory
 - message passing
- Finite memory
- Practicality issues
 - re-usable objects
- Performance



- Design (pseudo-code, prototype)

• Hardware

- Which atomic ops
- Memory consistency
- Cache coherence
- Locality
- Performance
- Scalability



Implementation (code)

Outline

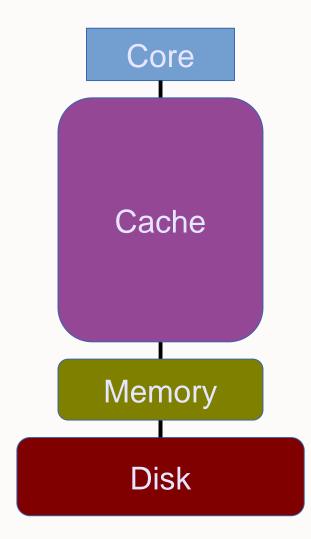
- CPU caches
- Cache coherence
- Placement of data
- Graph processing: Concurrent data structures

Outline

• CPU caches

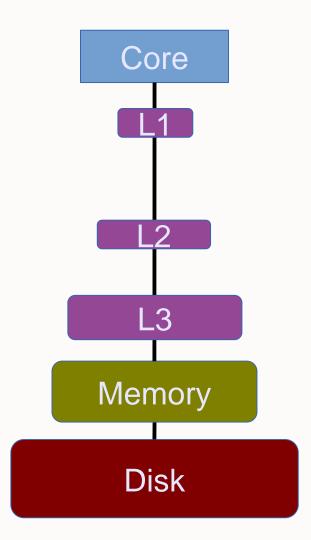
- Cache coherence
- Placement of data
- Graph processing: Concurrent data structures

Why do we use caching?



- Core freq: 2GHz = 0.5 ns / instr Core \rightarrow Disk = ~ms Core \rightarrow Memory = ~100ns Cache
 - Large = slow
 - Medium = medium
 - Small = fast

Why do we use caching?



- Core freq: 2GHz = 0.5 ns / instr Core \rightarrow Disk = ~ms Core \rightarrow Memory = ~100ns Cache
 - Core \rightarrow L3 = ~20ns
 - Core \rightarrow L2 = \sim 7ns
 - Core \rightarrow L1 = \sim 1ns

Typical server configurations

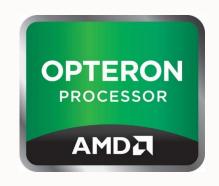
Intel Xeon

- 14 cores @ 2.4GHz
- L1: 32KB
- L2: 256KB
- L3: 40MB
- Memory: 512GB

AMD Opteron

- 18 cores @ 2.4GHz
- L1: 64KB
- L2: 512KB
- L3: 20MB
- Memory: 512GB





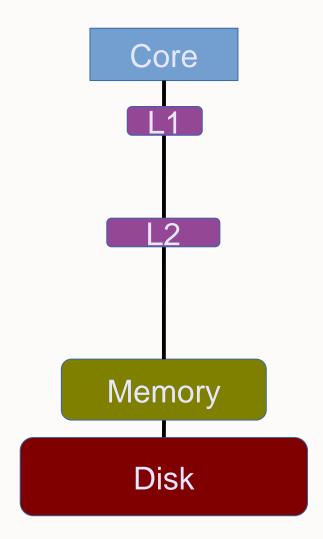
Experiment

Throughput of accessing some memory, depending on the memory size

Outline

- CPU caches
- Cache coherence
- Placement of data
- Graph processing: Concurrent data structures

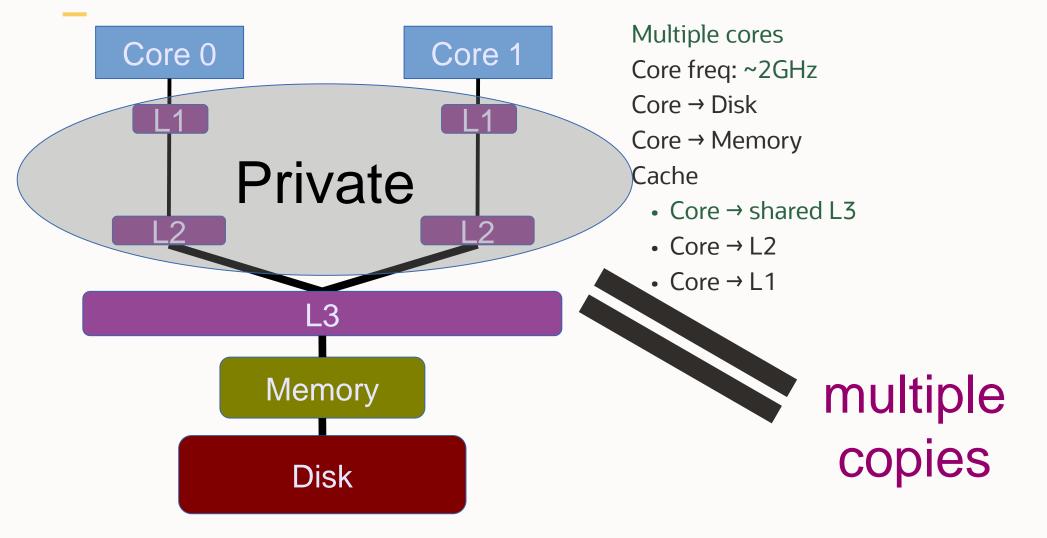
Until ~2004: single-cores



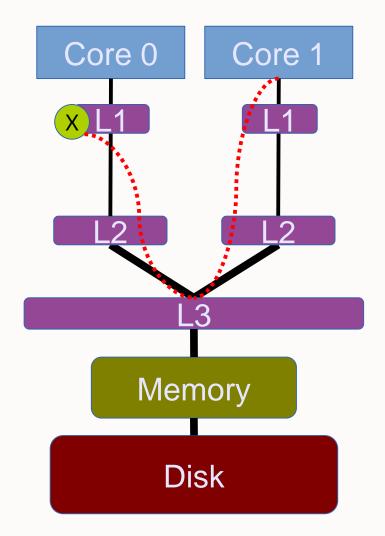
Single core Core freq: 3+GHzCore \rightarrow Disk Core \rightarrow Memory Cache \cdot Core \rightarrow L2

• Core \rightarrow L1

After ~2004: multi-cores



Cache coherence for consistency



Core 0 has X and Core 1

- wants to write on X
- wants to read X
- did Core 0 write or read X?

To perform a **write**

- invalidate all readers, or
- previous writer

To perform a **read**

• find the latest copy

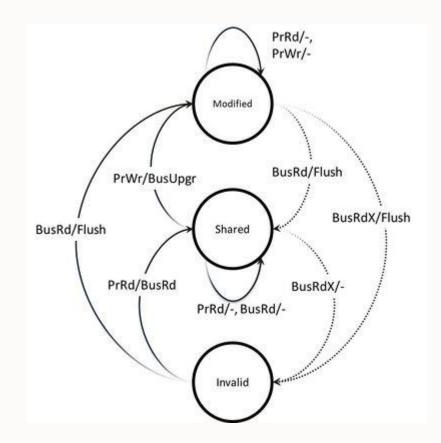
Cache coherence with MESI

A state diagram

State (per cache line)

- Modified: the only dirty copy
- Exclusive: the only clean copy
- Shared: a clean copy
- Invalid: useless data

Which state is our "favorite?"

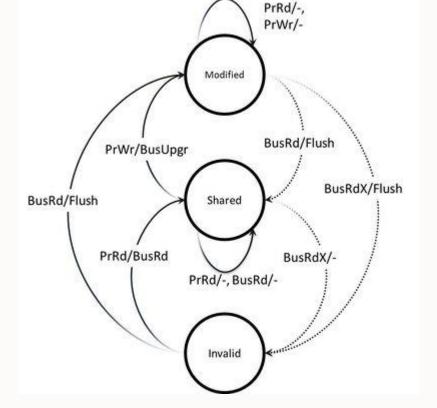


The ultimate goal for scalability

A state diagram

State (per cache line)

- Modified: the only dirty copy
- Exclusive: the only clean copy
- •Shared: a clean copy
- Invalid: useless data
- = threads can keep the data close (L1 cache)
 = faster



Experiment

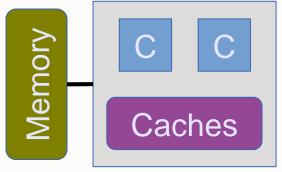
The effects of false sharing

Outline

- CPU caches
- Cache coherence
- Placement of data
- Graph processing: Concurrent data structures

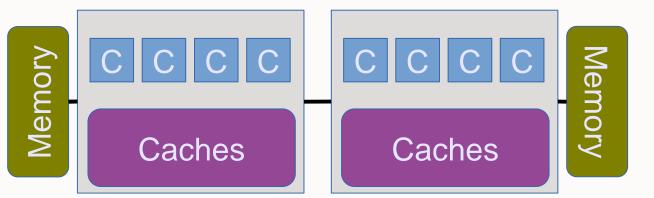
Uniformity vs. non-uniformity

Typical desktop machine



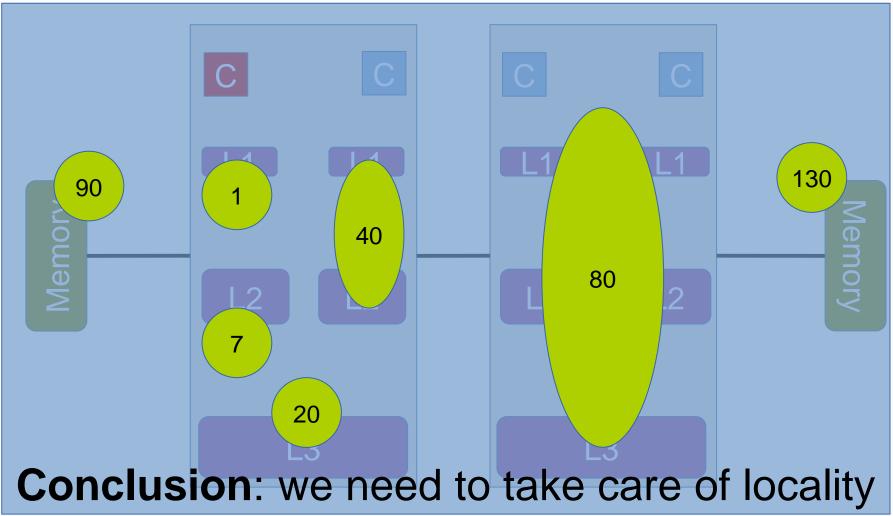
= Uniform

Typical server machine



= non-Uniform (aka. NUMA)

Latency (ns) to access data in a NUMA multi-core server



Experiment The effects of locality

Experiment The effects of locality

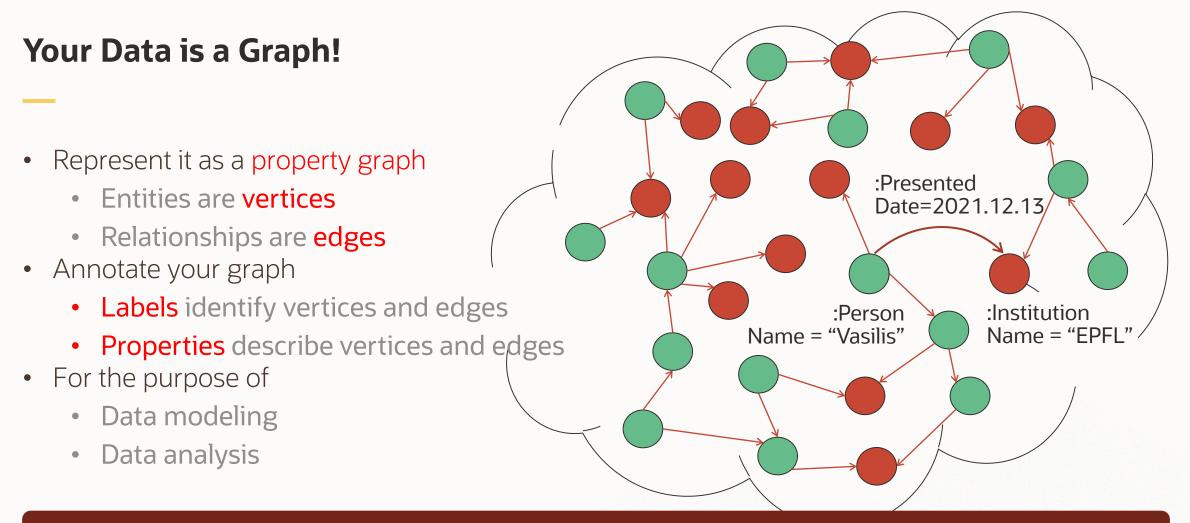
| <pre>vtrigona \$./test_locality</pre> | -x0 -y1 |
|--|--------------------------------------|
| Size: 8 counters | s = 1 cache lines |
| Thread 0 on core : 0 | |
| Thread 1 on core : 2 | |
| Number of threads: 2 | |
| Throughput : 104.27 M | 10p/s |
| | |
| | |
| <pre>vtrigona \$./test_locality</pre> | -x0 -y10 |
| | <u>-x0 -y10</u> 5 = 1 cache lines |
| | |
| Size: 8 counters | |
| Size: 8 counters Thread 0 on core : 0 | |

Same memory node

Different memory nodes

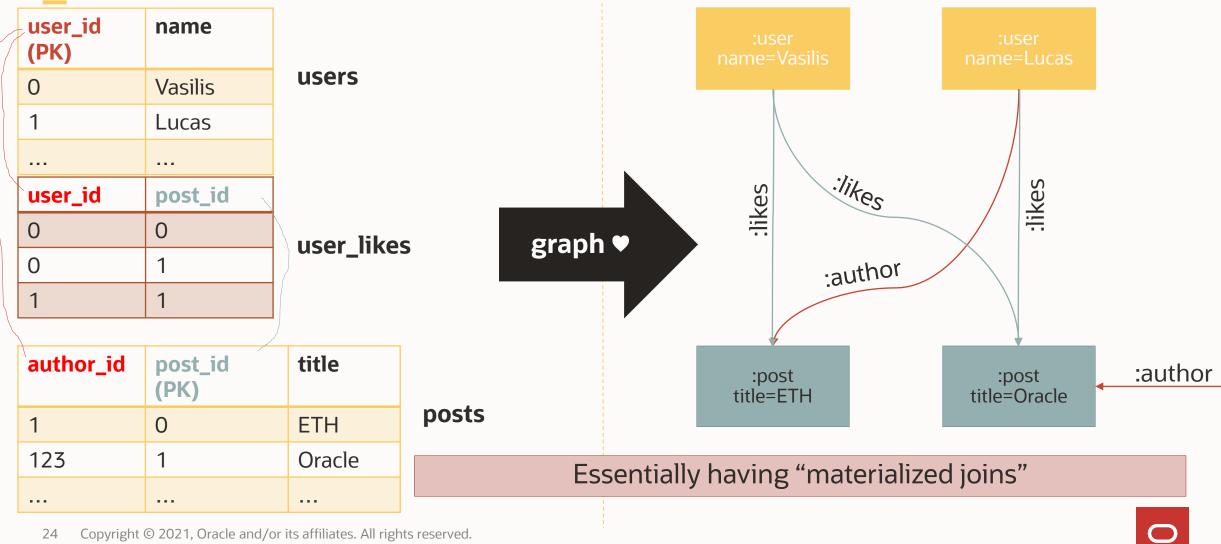
Outline

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Navigate multi-hop relationships quickly (instead of joins)

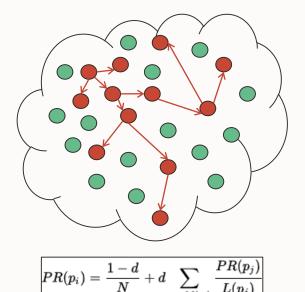
Relational (Database) Model \rightarrow Property Graph Model

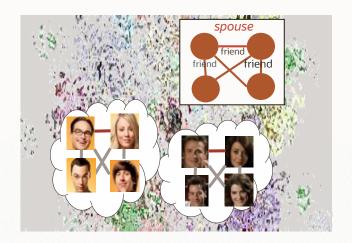


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Main Approaches of Graph Processing

- 1. Computational graph analytics [ASPLOS'12, VLDB'16]
 - Iterate the graph multiple times and compute mathematical properties using Greenmarl / PGX Algorithm (e.g., Pagerank)
 - **e.g**, graph.getVertices().forEach(n -> ...)
- 2. Graph querying and pattern matching [GRADES'16/17, VLDB'16]
 - Query the graph using PGQL to find sub-graphs that match to the given relationship pattern
 - **e.g.**, SELECT ... MATCH (a) -[edge]-> (b) ...
- 3. Graph ML (new)
 - Use the structural information latent in graphs
 - e.g., graph similarity





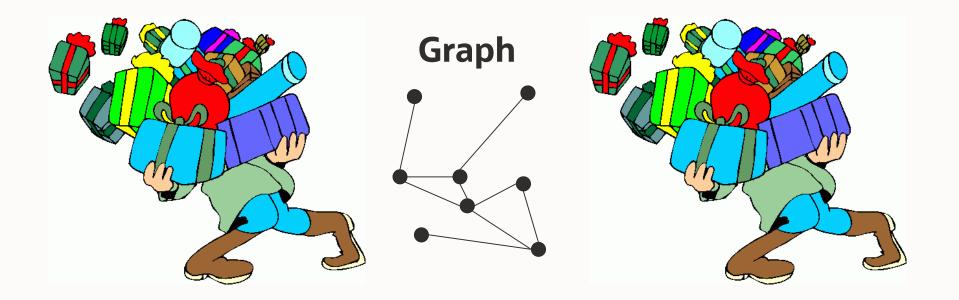
Dissecting a graph processing system

with a focus on (concurrent) data structures

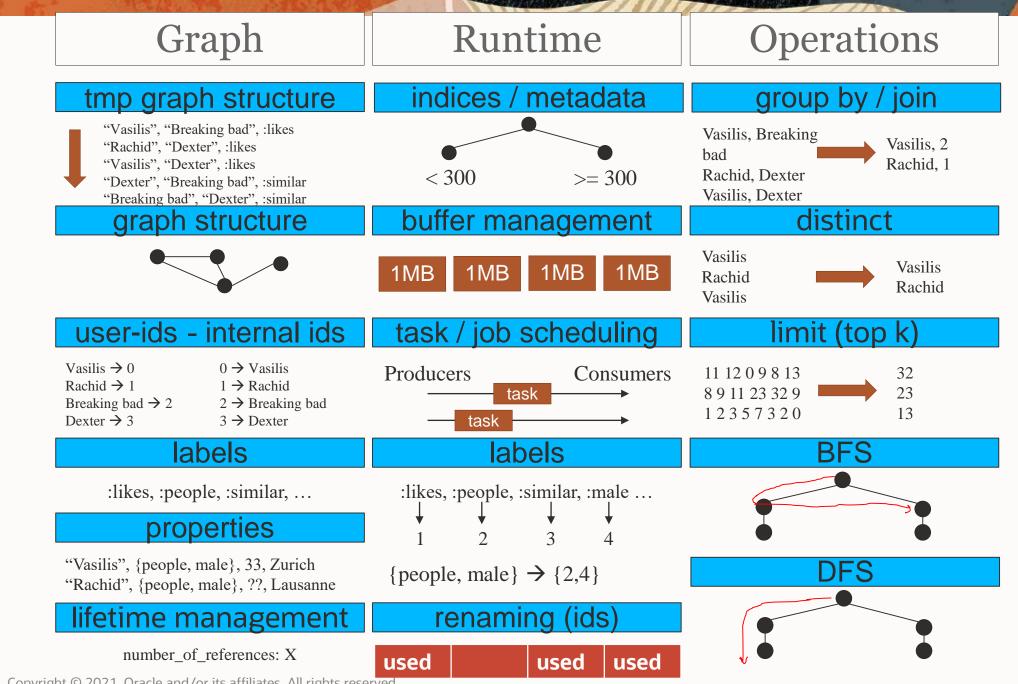
Dissecting a graph processing system and preparing for a job interview

with a focus on (concurrent) data structures

Architecture of a graph processing system

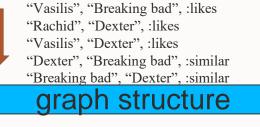


Tons of other data and metadata to store



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tmp graph structure





user-ids - internal ids

| Vasilis $\rightarrow 0$ | $0 \rightarrow Vasilis$ |
|------------------------------|-------------------------------|
| Rachid \rightarrow 1 | $1 \rightarrow \text{Rachid}$ |
| Breaking bad $\rightarrow 2$ | 2 \rightarrow Breaking bad |
| Dexter $\rightarrow 3$ | $3 \rightarrow \text{Dexter}$ |

labels

:likes, :people, :similar, ...

properties

"Vasilis", {people, male}, 33, Zurich "Rachid", {people, male}, ??, Lausanne

lifetime management

number_of_references: X

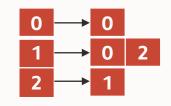
tmp graph structure

- append only
- dynamic schema
- \rightarrow segmented table
- Classic graph structures
 1. connectivity matrix
 - 0
 1
 2

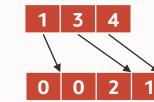
 0
 x
 ...

 1
 x
 ...

 2
 x
 x
 - 2. adjacency list



3. compressed source row (CSR)



tmp graph structure

"Vasilis", "Breaking bad", :likes "Rachid", "Dexter", :like Segin Chteckeouffer "Dexter", "Breaking bad", :similar "Breaking bad", "Dexter", :similar

graph structure



user-ids - internal idsVasilis $\Rightarrow 0$ $0 \Rightarrow$ VasilisRachid \Rightarrow Ish 2 $0 \Rightarrow$ VasilisBreaking tail \Rightarrow Breaking tail7 RachidDexter $\Rightarrow 3$ $3 \Rightarrow$ Dexter

:likes, :people, :similar, ...

properties

"Vasilis", {people, male}, 33, Zurich "Rachid", {people, male}, ??, Lausanne

lifetime management

number_of_references: X

Storing labels

- usually a small enumeration e.g., person, female, male
- storing strings is expensive "person" → ~ 7 bytes
- comparing strings is expensive

→ dictionary encoding, e.g.,

- person $\rightarrow 0$
- female \rightarrow 1
- male \rightarrow 2

Ofc, hash map to

- store those
- translate during runtime

tmp graph structure

"Vasilis", "Breaking bad", :likes "Rachid", "Dexter", :liker **SeginehteeteeDuffer** "Dexter", "Breaking bad", :similar "Breaking bad", "Dexter", :similar

graph structure

user-ids - internal idsVasilis $\rightarrow 0$ $0 \rightarrow Vasilis$ Rachinalsh map7RahinayBreaking bad2Dexter $\rightarrow 3$ $3 \rightarrow Dexter$ labels:likedigetio.martyr, ...

properties

"Vasilis", {people, male}, 33, Zurich "Rachid", {people, male}, ??, Lausanne

lifetime management

number_of_references: X

Property

- one type per property, e.g., int
- 1:1 mapping with vertices/edges
- \rightarrow (sequential) arrays
- Lifetime management (and other counters)
 - cache coherence: atomic counters can be expensive
 - Two potential solutions
 - 1. approximate counters

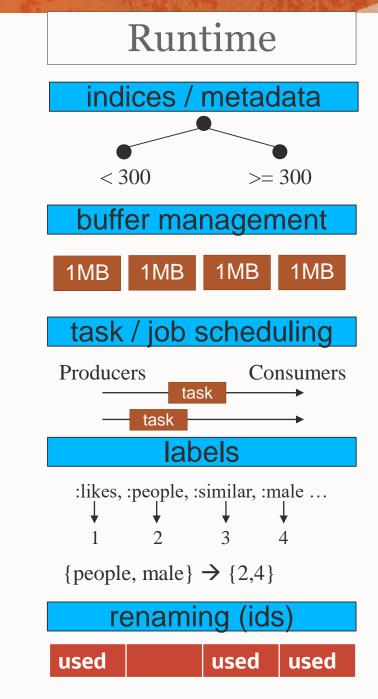
2. stripped counters

Thread local:counter[0]counter[1]counter[2]increment(int by) { counter[my_thread_id] += by; }
int value() {
int sum = 0;
for (int i = 0; i < num_threads; i++) { sum += counter[i]; }
return sum;

tmp graph structure "Vasilis", "Breaking bad", :likes "Rachid", "Dexter", :like Segmenteckebuffer "Dexter", "Breaking bad", :similar "Breaking bad", "Dexter", :similar graph structure user-ids - internal ids Vasilis $\rightarrow 0$ $0 \rightarrow Vasilis$ Rachihash 2map Ratitay Breaking bad 2 Dexter \rightarrow 3 $3 \rightarrow \text{Dexter}$ labels dictionary (=map) properties "Vasilis", {people, male}, 33, Zurich "Rachid", {people, male}, ??, Lausanne lifetime management number_of_references: X stripped counter

Score

| Structure | # Usages |
|----------------|----------|
| array / buffer | 5 |
| map | 2 |



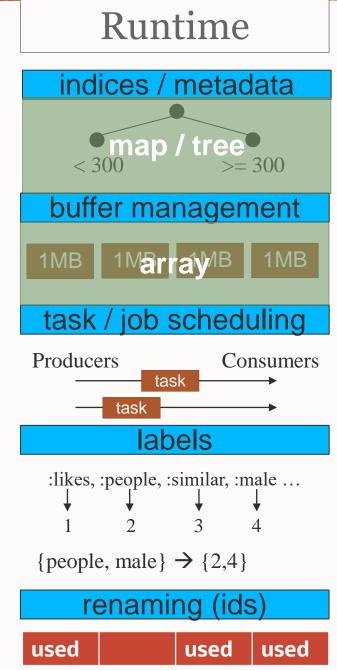
34

Indices

- Used for speeding up "queries"
 - Which vertices have label :person?
 - Which edges have value > 1000?

→maps, trees

- Buffer management
 - In "real" systems, resource management is very important
 - buffer pools
 - no order
 - insertions and deletions
 - no keys
 - → Fixed num object pool: array
 - → Otherwise: **list**
 - → Variable-sized elements: heap

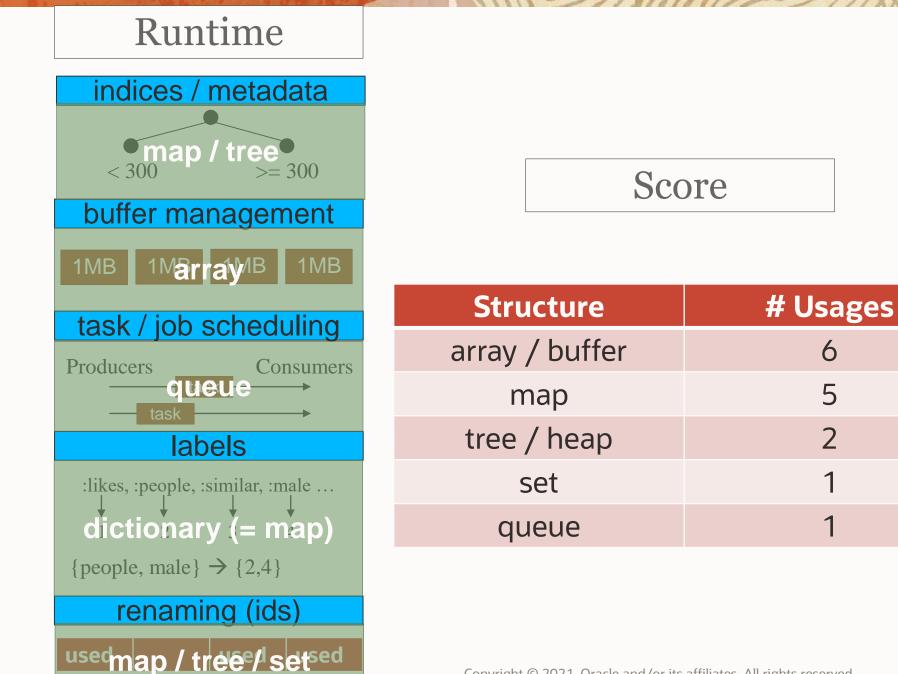


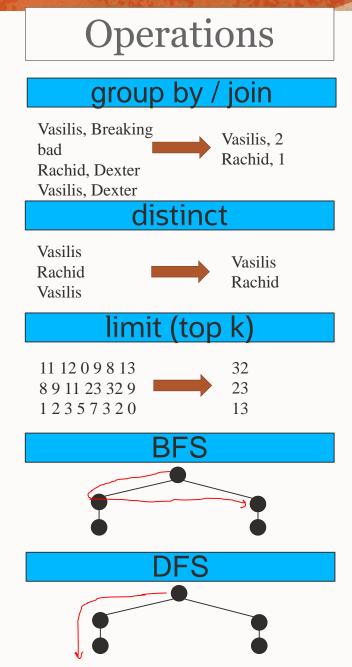
Task and job scheduling

- produces create and share tasks
- consumers get and handle tasks
- insertions and deletions
- usually FIFO requirements

 \rightarrow queues

- Storing / querying sets of labels
 - set equality expensive
 - usually common groups e.g., {person, female}, {person, male}
 - → 2-level **dictionary** encoding
 - {person, female} $\rightarrow 0$
 - {person, male} \rightarrow 1
- Giving unique ids (renaming) → tree, map, set, counter, other?

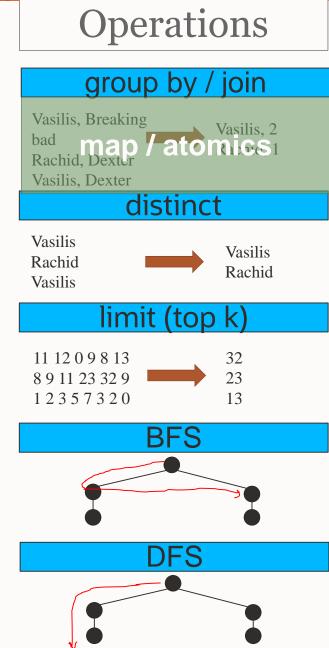




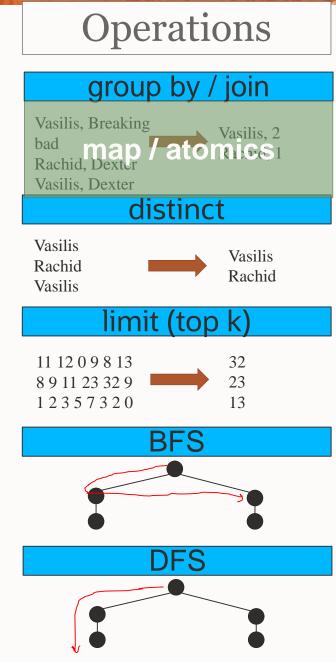
Group by

- 1. Mapping from keys to values
- 2. Atomic value aggregations e.g., COUNT, SUM, MAX
- insertion only
- ightarrow hash map
- → atomic inc / sum / max, etc.
- Join

- create a map of the small table
- insertion phase, followed by
- probing phase
- \rightarrow hash map, lock-free probing

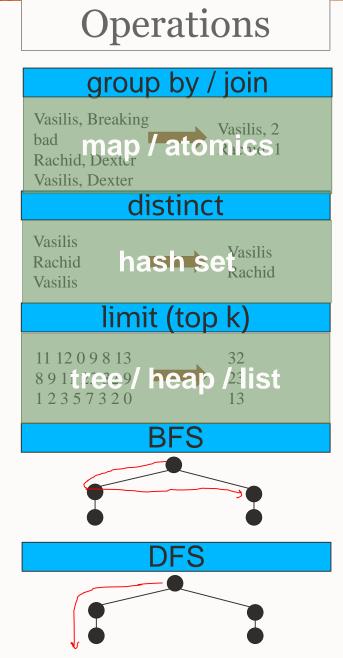


• can be solved with sorting, or



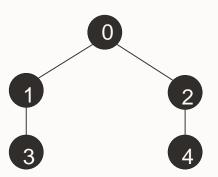
Distinct

- can be solved with sorting, or
 → hash set
- Limit (top k)
 - can be solved with sorting, or
 - different specialized structures
 - \rightarrow tree
 - \rightarrow heap
 - \rightarrow ~ list
 - → array (e.g., 2 elements only)
 - → **register** (1 element only)

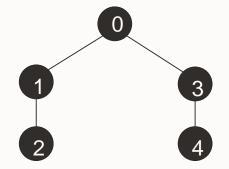


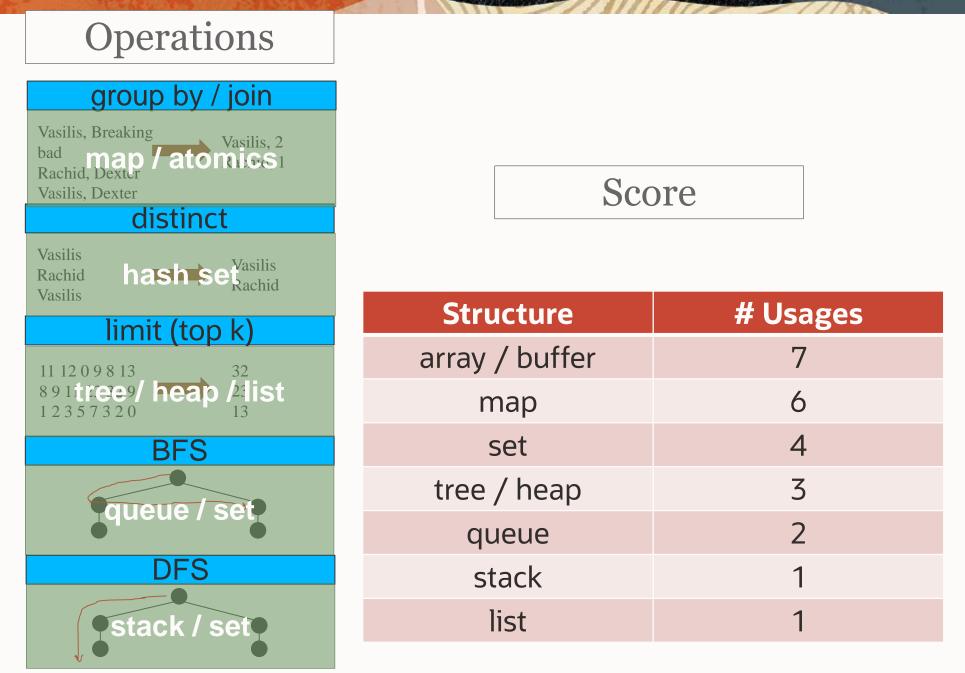
• Breadth-first search (BFS)

- FIFO order
- track visited vertices
- \rightarrow queue
- \rightarrow set

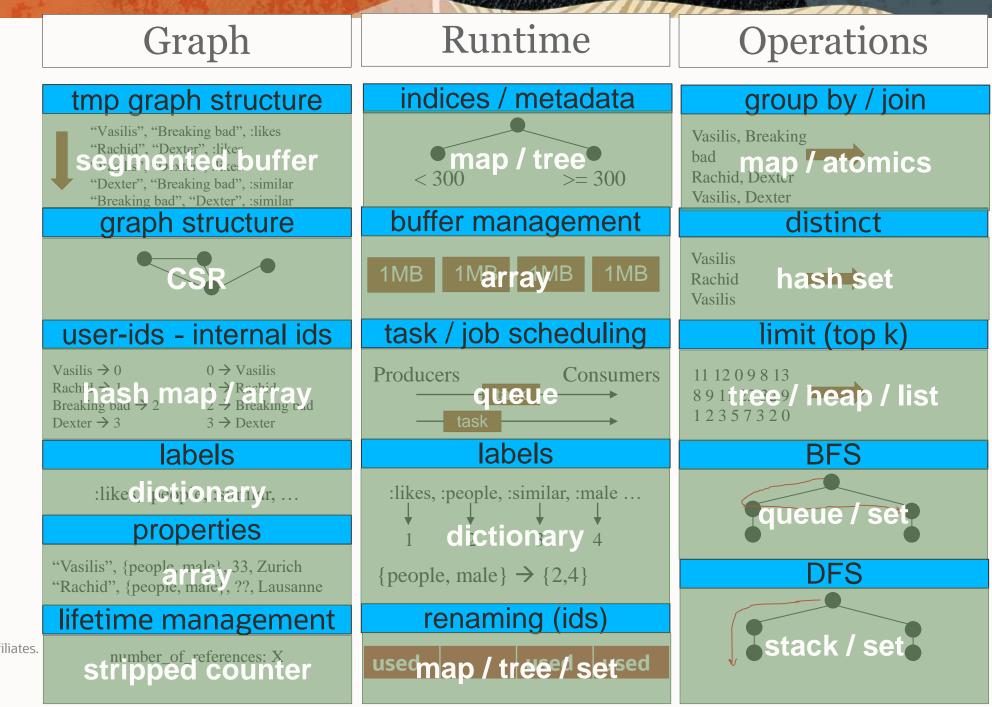


- Depth-first search (DFS)
 - LIFO order
 - track visited vertices
 - \rightarrow stack
 - \rightarrow set





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Your new cheatsheet for interviews!

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Conclusions

- Both theory and practice are necessary for
 - Designing, and
 - Implementing fast / scalable data structures
- Hardware plays a huge role on implementations
 - How and which memory access patterns to use
- (Concurrent) Data structures
 - The backbone of every system
 - An "open" and challenging area or research

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Maleson