Concurrent Algorithms 2009: Exercise 8

November 17, 2009

1 Question 1

Is it possible to implement a compare-and-swap object using (any number of) base compare-and-swap objects of which one can be non-responsive?

2 Question 2

Is it possible to implement a SWMR atomic register using (any number of) base SWMR atomic registers of which t can be non-responsive?