



Register implementations

- Algorithm 1: implements a SWMR register out of t+1 SWMR base responsive failureprone registers
- Algorithm 2: implements a SWSR register out of 2t+1 SWSR base non-responsive failure-prone registers
- Algorithm 3: implements a C&S object out of t+1 base responsive failure-prone C&S



- Write(v)
 - For j = 1 to (t+1) do
 Reg[j].write(v);
 - return(ok)
 - · ·
- Read()
 - **•** For j = t+1 to 1 do
 - v := Reg[j].read();
 if v ≠ ⊥ then return(v)
 - $1 \neq \perp$ then return(v)





Responsive model (single-shot compare&swap)

- r r := v;
 for j = 1 to t+1 do
 r' := CS[i].C&S
- r' := CS[j].C&S(r);
 f if r' ≠ ⊥ then r := r';
- return(r)

Exercises

- (1) Is it possible to build a C&S with base C&S objects among which one can be nonresponsive?
- (2) Build a SWMR register that tolerates t non-responsive base SWMR registers

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